

ADMISSIONS OPEN FALL 2024 MS Electrical Engineering Specialization in Integrated Circuits (IC) & Systems



"A Training from Idea to Product Design"

Department of Electrical Engineering at FAST-NUCES ISB offers an MS program with expertise in Integrated Circuits and Systems . Pioneer and most mature IC Design MS program in Pakistan, student will do the real-world tape out from project conception to the working silicon microchip.

WHY INTEGRATED CIRCUITS (ICs) DESIGN ?	PROGRAM AND COURSES OUTLINE			
 High demand for skilled IC designers in global 500 Billion \$ industry Recent IC design center established in the Public Sector Organizations. More than six IC startups in Pakistan over the last two years. 	Integrated Circuits Master's Program			
WHY from FAST NUCES ?	Semester-1 Fall 2024	Semester-2 Spring 2025	Semeter-3 Fall 2025	Semester-4 Spring 2026
First and most established program in Pakistan having licensed Cadence tools suite and TSMC 28nm, 65nm, 130nm, & 180nmPDKS. 8 graduate level courses purely related to IC Design, covering all domains of IC Design from Analog, Digital, Mixed Signal. All of the MS ICD batches have 100% employment track record. More than 200 hours training of graduates on Cadence Tool Suite. Program tailored to train engineers in 2 semesters for Semiconductor Industry entry.	 1. Research Methodology 2. Digital Integrated Circuits 3. Analog & DT ICs 4. Elective-I 2 Semester (Morning Course Work (Aug-2) Note: Electives are solution of the second secon	2. Elective-II 3. Elective-III 4. Elective-IV Program) On-Campus 2024 to June-2025)	or 3. ElectiveV 4. Elective-VI Students Free to Join Option-1: Opt indutria Option-2: Opt thesis, & Option-3: 6 credit cour completion in 3 Semes	I project. No campus visit 8-16 hours/week on-campus rse work (Degree
 Proven track record of more than a dozen fabricated graduate projects in Pakistan with three graduate student batches. International collaboration with Si-Valley company. Active collaboration with International Universities. 	20 Graduate Students Tapeout Projects (Sessions 2021, 2022, & 2023) TSMC 65nm Tapeouts 2021 PICO 130nm Tapeouts 2021 Kamal-1 Kamal-2 2021			
Feedback from Alumni and Industry				2.92 mm

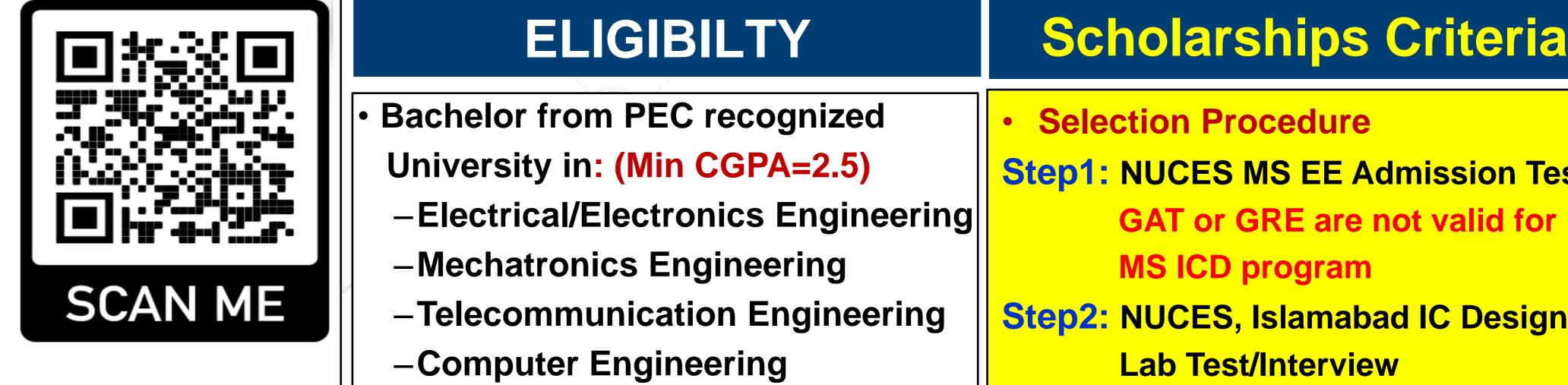
- The courses were specifically designed to provide the students with a strong knowledge of electronics and IC design skills. (Dr. Nasir, Sr Director, NECOP)
- This program is perfect for Engineers seeking in-depth training from concept to tape-out in analog and digital IC design. (Aziz ur Rehman GM, NECOP)
- One thing I must say! After working with the faculty, if you put your whole effort too, you will be a practical Chip designer. (Hamza Saleem, MS IC)
- The program offered me a variety of skills including design, plan, management, economy, and art of designing practical Integrated circuits, (M Usman, MS IC)

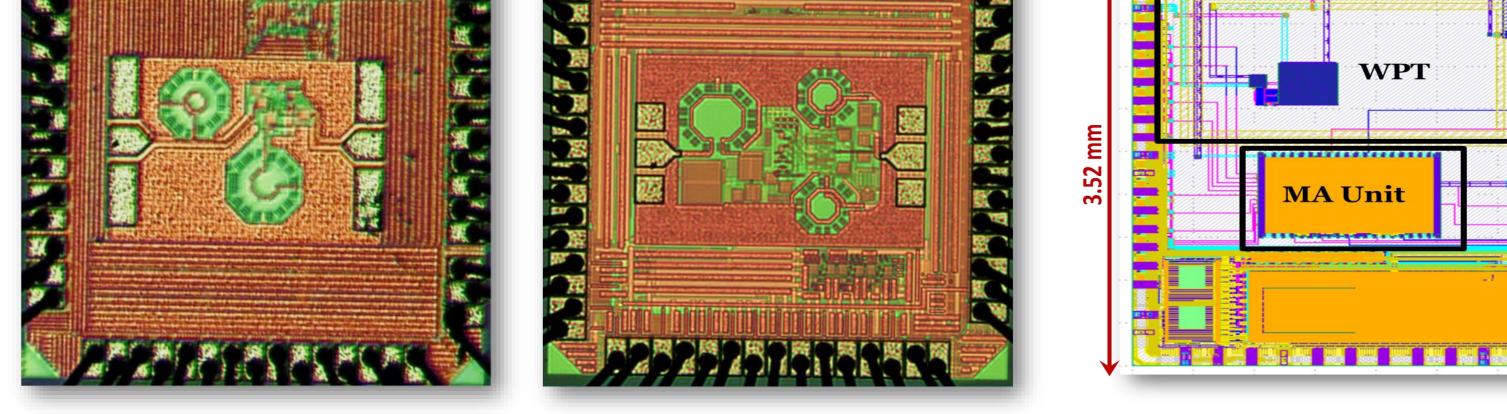
Teaching and Technical Staff

Prof. Dr. Rashad Ramzan M.S. Royal Institute of Technology Stockholm, Germany Ph.D. Linkoping University, Sweden Professor at Department of Electrical Engineering FAST, NUCES, ISB.	M.S. & Ph.D. Sungkyunkwan University, South Korea. Asst. Professor at Department of Electrical Engineering	Engr. Aqsa-Ehsan MS EE Design Engineer FAST, NUCES, ISB. Engr. Fari-ud-Din Lab Engineer FAST, NUCES, ISB.
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HOW TO APPLY

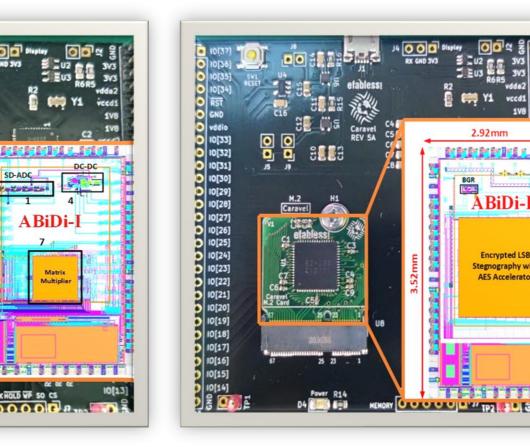
Apply Online at





1) Approximate ALU, 2) mm-Wave LNA, 3) Triboelectric Energy Harvester, 4) AC Logic, 5) mm-Wave Phase Shifter, 6) Sigma-Delta ADC 7) True Random Number Generator, 8) Configurable Logic Block, 9) Bi-Directional Amplifier, 10) WPT, 11) Multiply-Add Unit

PICO 130nm Tapeouts Nov, 2022



PICO Accepted Tapeouts 2023

st of Accepted Projects:			
Name	Affiliation	Notebook Title	
Junbeom Park	SNU, South Korea	10-Bit 100-MS/s Current-Steering DAC using IEEE SSCS Open-Source Tools	
Aya Khaled	Egypt	Current steering DAC	
Gabriel Maranhão	Universidade Federal de Santa Catarina, Brazil	Analog blocks	
Akira Tsuchiya	The University of Shiga Prefecture, Japan	Development of Process Portable Design Automation for Analog Circuits	
Nouman Ahmad	National University of Computer Emerging Sciences, Pakistan	CURRENT STEERING DIGITAL TO ANALOG CONVERTER	
Juan Sebastián Moya Baquero	Universidad Industrial de Santander	11-bit differential ADC SAR	
Jorge Marín	Universidad técnica Federico Santa María, Chile	PMIC for lab-bench-on-chip	
Jorge Marín	Universidad técnica Federico Santa María, Chile	12-bit SAR ADC design for a lab-bench-on-chip	
Sanaullah Hukam	National University of Computer Emerging Sciences, Pakistan	Phase Locked Loop Based Tunable Clock Generator for ADC and DAC	
Mubeen Yousaf	Fast National University, Pakistan	Scope Mux and Signal Conditioning	
Numan Ijaz	National University of Computer And Emerging Sciences, Pakistan	12-BIT PIPELINE ANALOG TO DIGITAL CONVERTER (AD	
Muhammad Umar	Fast National Univeristy, Islamabad, Pakistan	ARBITRARY WAVEFORM GENERATOR MUX AND SIGNA CONDITIONING	
Hafiz Azeem Abbas	National University of Computer And Emerging Sciences, Pakistan	A Low Phase-Noise Tunable Differential Clock Generator	

